



ADITYA DEGREE COLLEGE, KAKINDA

PEFINAL EXAMINATION B.Sc II SEMESTER – ELECTRONICS

TOTAL TIME:3Hrs

TOTAL MARKS:75

SECTION-A

Answer any Four questions

5 x 5=25m

1. Explain vactor diode in detail.
2. What is Depletion Region & Explain?
3. Explain Transistor as a switch?
4. Explain working of photo diode?
5. Write about 3-Terminal Regulator?
6. Explain in detail voltage Divider bias
7. Write a short note on Brige Full ware Rectifier
8. Explain working & Characteristics of 'LED'?

SECTION-B

Answer all questions

5 x 10=50m

9. (a) Discuss the construction & working of P-N Junction diode Draw & explain it's Characteristics.

(OR)

(b) Write in detail Tunnel Diode

10. (a) Explain i/p & o/p characteristics of BJT in CE configuration

(OR)

(b) Explain i/p & o/p characteristics of CB Configuration of Transistor

11. (a) Explain functioning of JFET & Discuss it's characteristics

(OR)

(b) Explain the working of SCR and it's V-I Characteristics

12. (a) Describe the working of Photo Transistor and Explain its characteristics?

(OR)

(b) Explain the structure & operation of LDR

13. (a) Draw the circuit diagram of FULL WAVE RECTIFIER. Obtain expression for efficiency 4 Ripple factor

(OR)